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V Lun, IM MacLeod - IEEE TRANSACTIONS ON SYSTEMS, MAN, AND ..., 1992

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... Consider a transfer from Network A (input) to Network B (output). The input slave can **write** to the A_in_FIFO as long as the Programmable Almost Full (PAF) flag is not set. The output **master** can **read** from the B_out_FIFO as long as the Empty Flag is not set. ...Cited by 1 - [Related articles](#) - [All 16 versions](#)[psu.edu](#) [PDF]**Digital trimming of on-chip analog components**

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... 1 0 A9 A8 R/W ACK A7A6A5A4A3A2A1 AO ACK | S = START CONDITION R/5 = **READ/WRITE** PULSE ACK = ACKNOWLEDGE BY * = 0 FDR **WRITE** FIG.15 ... FROM ~ **MASTER** S ACKNOWLEDGE-^ .16 CON TAR T CLUCK PULSE FDR j IDN ACKNOWLEDGEMENT — ' on ...Cited by 7 - [Related articles](#)**Method and apparatus for interfacing between peripherals of multiple formats and a single system bus**

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